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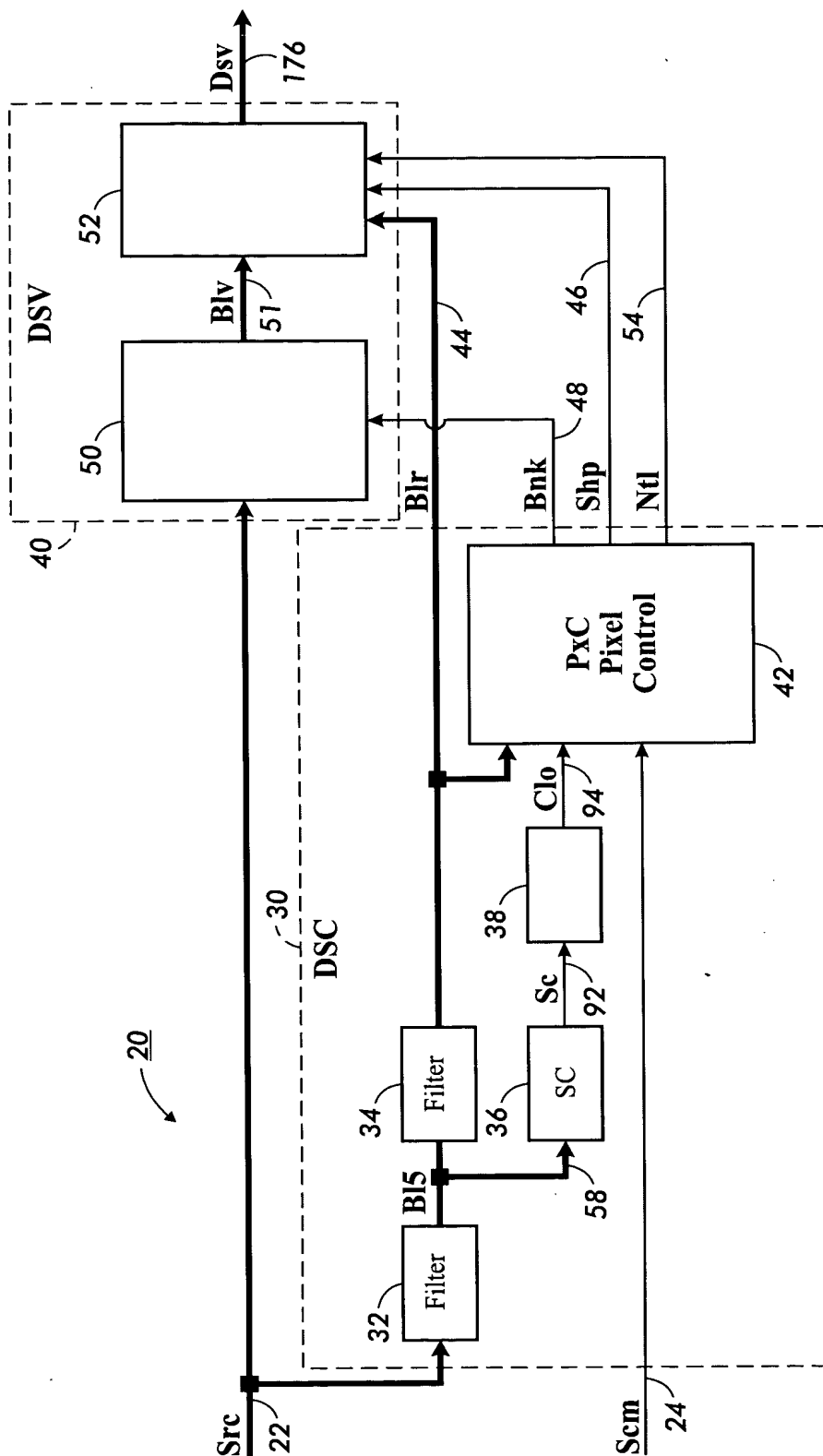


FIG. 1

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FIG. 2

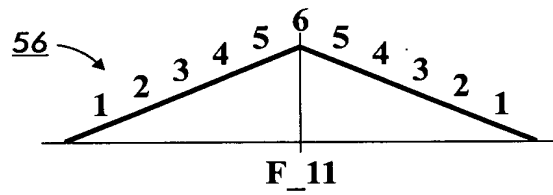
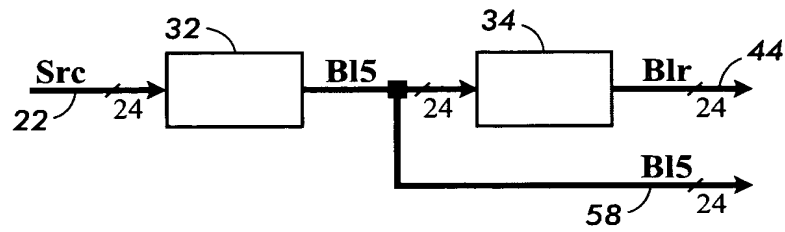


FIG. 3

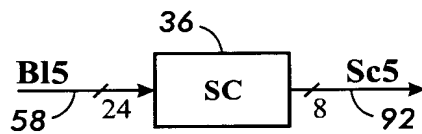


FIG. 4

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FIG. 5

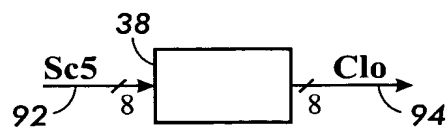
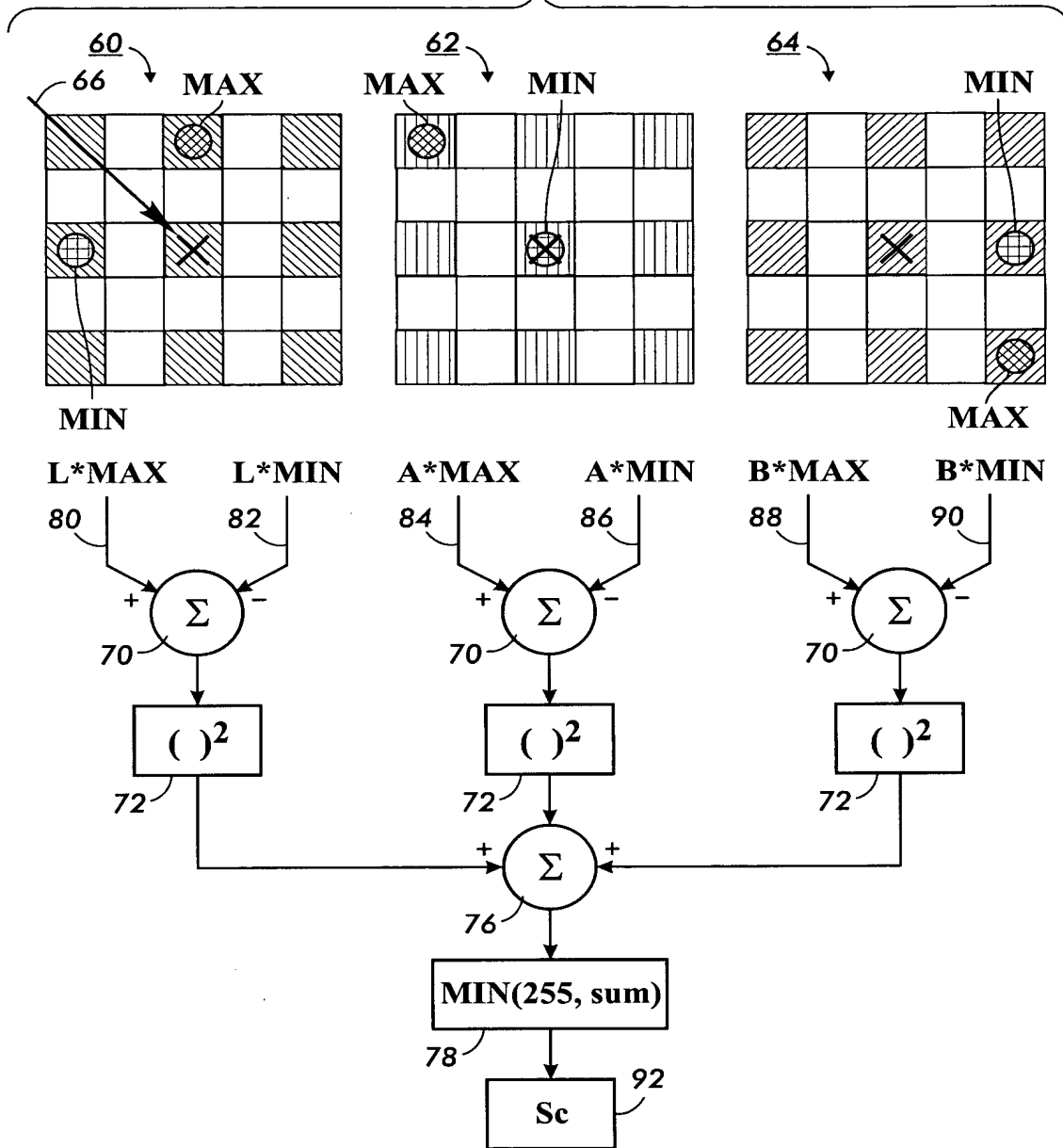
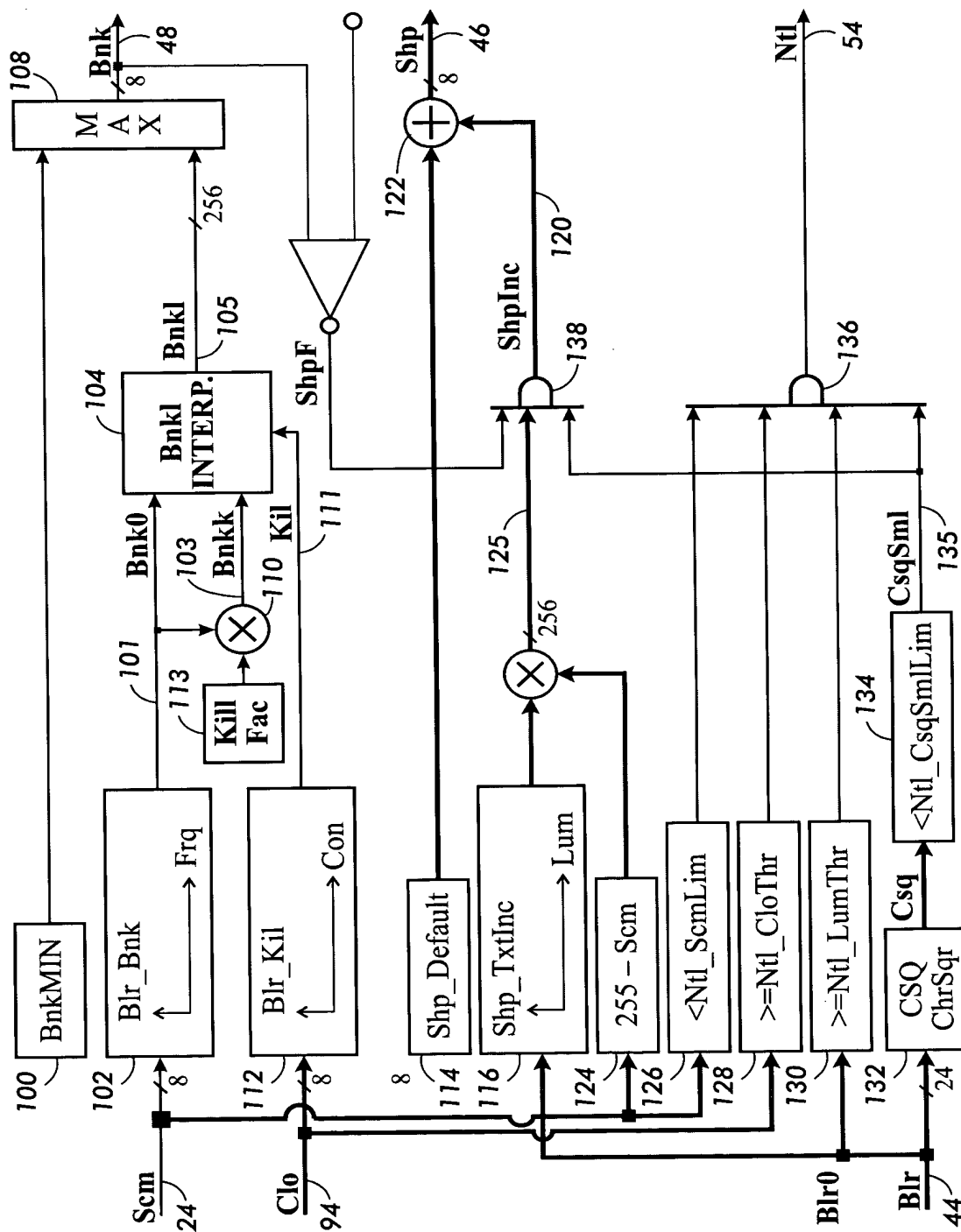


FIG. 6



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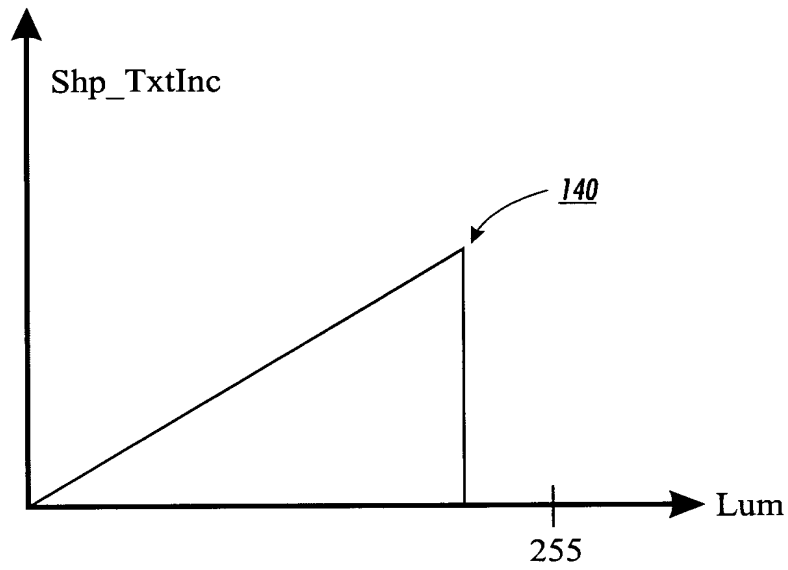


FIG. 8

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FIG. 9

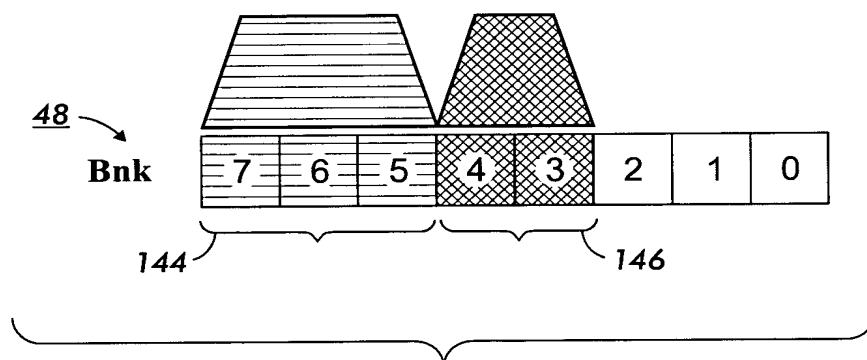
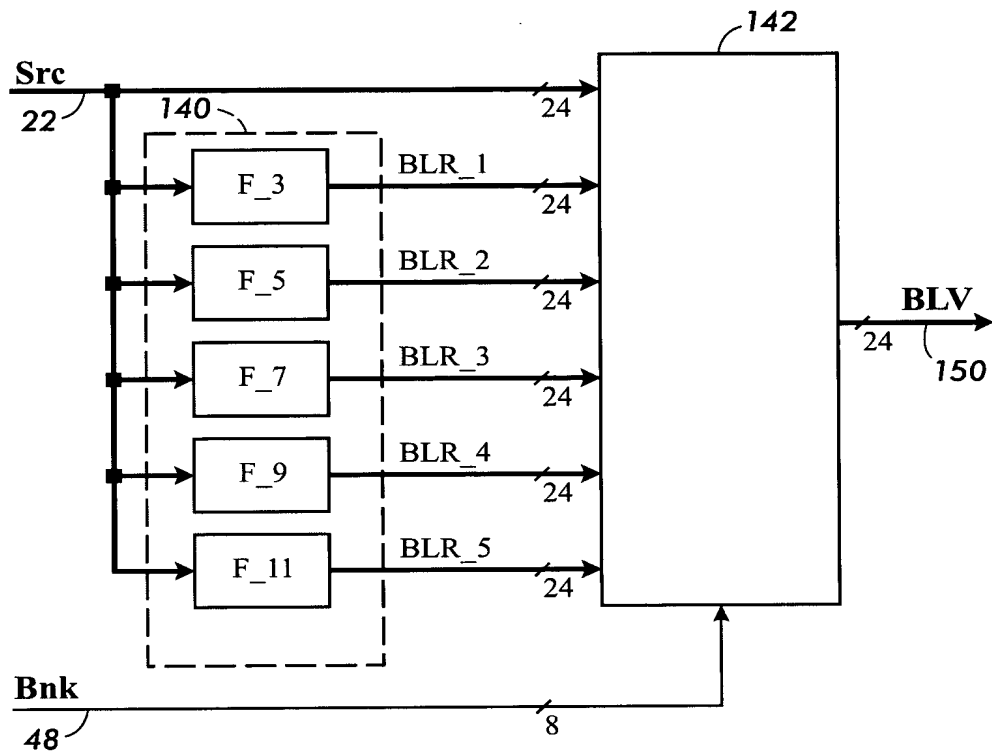
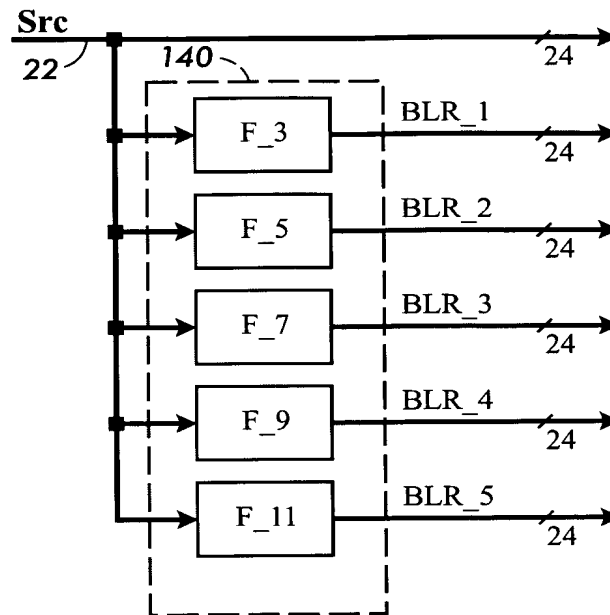


FIG. 10

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FIG. 11

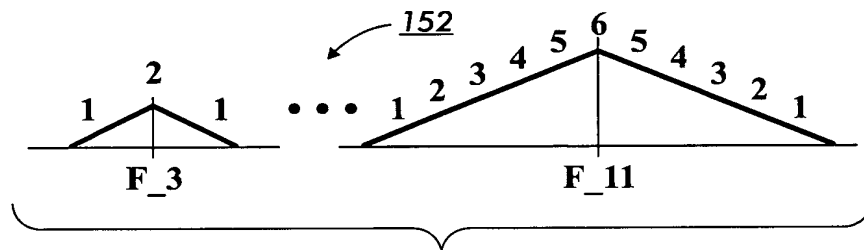
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Bnk bit 7	Bnk bit 6	Bnk bit 5	
0	0	0	Src and Blr_1
0	0	1	Blr_1 and Blr_2
0	1	0	Blr_2 and Blr_3
0	1	1	Blr_3 and Blr_4
1	0	0	Blr_4 and Blr_5
1	0	1	Blr_5
1	1	0	
1	1	1	

FIG. 12

FIG. 13162 →

0	1	1	1	1	<<3
1	3	4	16	1	>>1
2	5	9	81	809	>>13
3	7	16	256	1	>>5
4	9	25	625	839	>>16
5	11	$36=9*4$	$1296=81*16$	809	>>17

**FIG. 14**

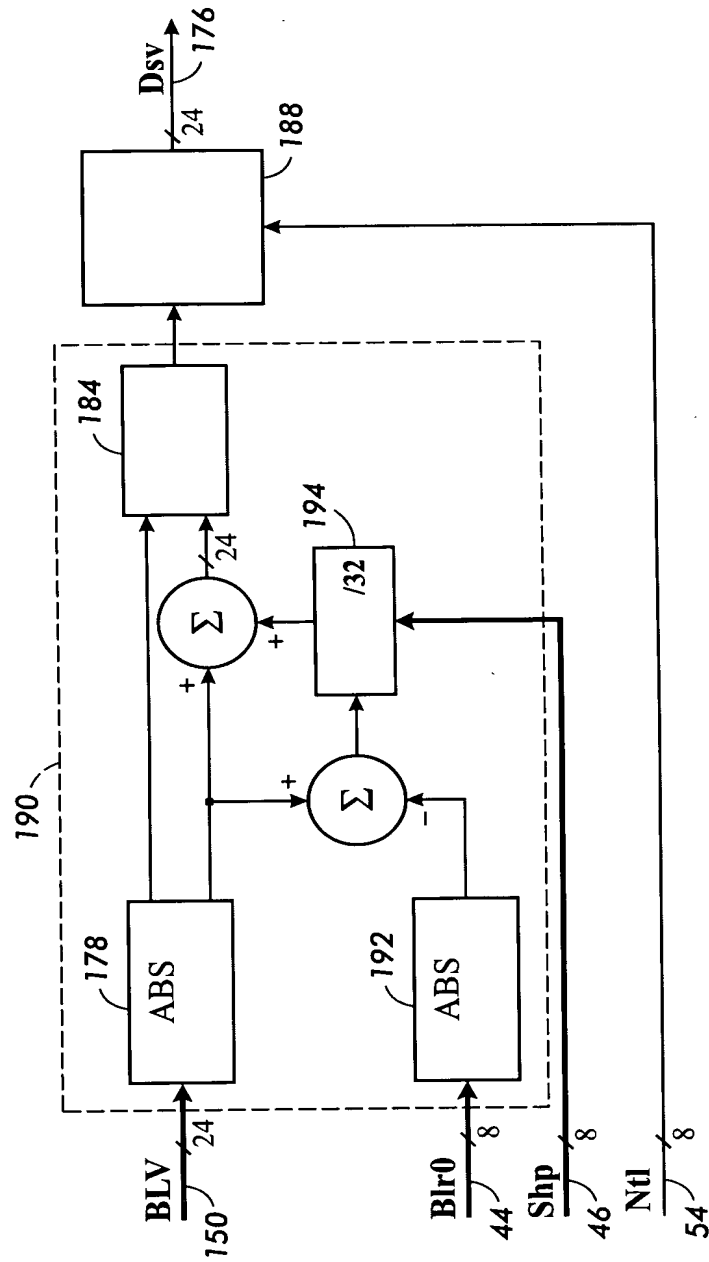


FIG. 15